Chettinad College of Engineering & Technology, Karur

Department of Electronics and Communication Engineering

News Report

Programme Name: Workshop on "Digital Design with VERILOG HDL and XILINX VIVADO"

Resource Person(s): Mr.V.Govindaraj, Alumnus of Chettinad Tech

Faculty Incharge : Mr.P.Selvan, Sr.AP/ECE

Date: 16-03-2024 & 17-03-2024

Venue: 'C' Block- DSP Laboratory

Number of Participants: 43

Description:

        The Department of Electronics and Communication Engineering recently organized a workshop focusing on Digital Design with Verilog HDL and XILINX VIVADO for III ECE students, conducted by our alumnus Mr. V. Govindaraj. The sessions were designed to provide students with practical insights and hands-on experience in digital VLSI.

        The instructional approach adopted for the workshop prioritized activity-based learning. This methodology empowers students to engage directly with the material, fostering a deeper understanding of the concepts. By actively participating in the sessions, students not only absorbed information more effectively but also honed their skills through practical application.

        The workshop covered a range of topics essential to digital design using Verilog HDL and VIVADO. These included understanding Verilog HDL, the chip fabrication process, various styles of modeling, and test bench architecture. By delving into these subjects, students gained a comprehensive overview of fundamental concepts crucial for success in the field of

digital VLSI design.

The knowledge and skills acquired during this workshop serve as a solid foundation for students as they continue their journey in electronics engineering. Such initiatives not only enhance academic learning but also prepare students for real-world challenges in their chosen field.

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